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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1

of 2

Application Number

10/675,549

Filing Date

9/30/2003

First Named Inventor

Jong-Hoon Oh

Group Art Unit

2824

Examiner Name

Van Thu Nguyen

Attorney Docket Number

INFN/SZ0021

Submission Date

July 27, 2005

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	A1	US-			
	A2	US-			
	A3	US-			
	A4	US-			
	A5	US-			
	A6	US-			
	A7	US-			
	A8	US-			
	A9	US-			
	A10	US-			
	A11	US-			
	A12	US-			

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ³
		Number-Kind Code ² (if known)				
VTN	B1	EP 0 344 886 A1	12-06-1989	International Computers Limited		
	B2					
	B3					
	B4					
	B5					

Examiner

Van Thu Nguyen

Date Considered

8/18/05

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Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	10/675,549		
		Filing Date	9/30/2003		
		First Named Inventor	Jong-Hoon Oh		
		Group Art Unit	2824		
		Examiner Name	Van Thu Nguyen		
		Attorney Docket Number	INFN/SZ0021		
Sheet	2	of	2	Submission Date	July 27, 2005

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
VTN	C1	PCT International Search Report and Written Opinion dated June 8, 2005.	
	C2	General DDR SDRAM Functionality, Micron Technical Note TN 46-05, Micron Technology, Inc., July 2001, pp. 1-11.	
	C3	KDM432D2131 – 64Mbit DDR SGRAM, 512K x 32Bit x 4 Banks, Double Data Rate Synchronous Graphic RAM with Bi-directional Data Strobe, Samsung Electronics Target Specification, Revision 0.3, May 1999, pp. 1-49.	
VTN	C4	C. H. KIM et al., A 64-Mbit, 640-MByte/s Bidirectional Data Strobed, Double-Data-Rate SDRAM with a 40-mW DLL for a 256-Mbyte Memory System, IEEE Journal of Solid-State Circuits, Vol. 33, No. 11, pp. 1703-1709, Nov. 1998.	
	C5		
	C6		
	C7		
	C8		
	C9		
	C10		

Examiner <i>Van Thu Nguyen</i>	Date Considered <i>8/18/05</i>
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